

## WHAT IS CLAIMED IS:

1. An image processor comprising:  
a plurality of function blocks connectable to each other and dealing image data;

5 an interface connected to a network; and  
a bus changer which changes bus connection among said plurality of function blocks and said interface.

2. The image processor according to claim 1, wherein  
10 said plurality of function blocks comprise an image input block which receives image data, an image processing block which deals image data, and an image output block which outputs the image data.

3. The image processor according to claim 2, wherein  
15 said image input block receives image data read with an image sensor.

4. The image processor according to claim 2, wherein  
said image output block prints an image on a registering medium.

5. The image processor according to claim 2, wherein  
20 said bus changer image input block changes the bus connection such that image data from said network is received through said interface and sends image data to said network through said image output block or said interface.

25 6. The image processor according to claim 1, wherein

The following are the names of the persons who have been appointed to the various positions in the various departments of the Government of the State of New York, for the year ending December 31, 1900:

*[Signature]*

*[Handwritten signature]*

an interface connected to a network;  
a bus changer which changes bus connection among  
said plurality of function blocks and said interface; and

10. The image processor according to claim 9, further comprising a power supply controller which supplies electric power to function blocks to be operated in said plurality of function blocks.

11. The image processor according to claim 10,  
wherein said power supply controller stops power supply to  
said function blocks after processing in said function

*3/2* ~~blocks is completed.~~

12. The image processor according to claim 9, wherein said plurality of function blocks comprise an image input block which receives image data, an image processing block which deals image data, and an image output block which outputs the image data.

*3/2* 13. An image processor comprising:

a plurality of function blocks connectable to each other and dealing image data;

an interface connected to a network;

a bus changer which changes bus connection among said plurality of function blocks and said interface;

a memory having a function management table to manage executable functions; and

a controller which requests an external apparatus connected through said interface and said network to operate a function when the function is not managed in the function management table in said memory.

14. The image processor according to claim 13, wherein one of said function blocks comprises a memory which stores an application program, and a controller which processes the image data according to the application program.

15. The image processor according to claim 14, wherein said memory has a capacity which stores another

application program further.

16. A method of controlling image processing in an image processor including a plurality of function blocks, comprising following steps of:

- 5 receiving a request to perform a function;  
deciding whether the function is executable in said image processor; and  
changing bus connection between a necessary function block and said interface to operate an external apparatus connected through an interface connectable to said network when the function is decided not executable in said image processor.

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17. The method according to claim 16, wherein the decision is performed with reference to a management table provided to manage executable functions stored in a memory.

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18. The method according to claim 16, further comprising the step of sending a signal to request execution of the function to the external image processor.

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